

withdraw the appropriate fee under 37 C.F.R. §§ 1.16 to 1.21 from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.050800. Reconsideration of the application in view of the following amendments and remarks is respectfully requested.

AMENDMENT

Please cancel claims 2, 5, 10-16, 18, and 19 without prejudice or disclaimer.

Please amend claims 1, 3, 4, and 17 as follows.

A1

1. (Amended) A system, comprising:

- a power supply configured to provide a standby signal and receive a power up signal;
- a detection circuit coupled to receive the standby signal and output a power on signal for the power supply in response to receiving the standby signal;
- a delay circuit coupled to receive the power on signal, wherein the delay circuit is configured to provide the power up signal to the power supply after a predetermined delay in response to receiving the power on signal; and
- a stabilizer circuit coupled between the standby signal and the power up signal, wherein the stabilizer circuit is configured to provide a stable transition in the receipt of the power up signal; and

wherein the delay circuit is configured to provide the delayed power on signal to the power supply as the power up signal once the predetermined period of time has passed since the delay circuit received the power on signal; and

wherein the stabilizer circuit is further configured to receive the standby signal and to provide the standby signal to the power supply as the power up signal to keep the power up signal inactive.

Sub B1

3. (Amended) The system of claim 1, further comprising:

- an integrated circuit coupled to receive the standby signal from the power supply.

A2

4. (Amended) A system, comprising:

- a detection circuit configured to receive a standby signal from a power supply and to deliver a control signal;

Sub B1

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a delay circuit coupled to receive the control signal and to deliver a delayed control signal for the power supply in response to the control signal after a predetermined period of time;

a stabilizer circuit configured to receive the standby signal and to receive the delayed control signal, wherein the stabilizer circuit is further configured to provide the delayed control signal to the power supply to ensure a stable transition during the receipt of the delayed control signal by the power supply.

17. (Amended) A system, comprising:

an integrated circuit;

Sub B1
a power supply coupled to provide power to the integrated circuit, wherein the power supply is further configured to provide a standby signal to the integrated circuit, wherein the power supply is further configured to receive a power up signal;

a detection circuit coupled to receive the standby signal, wherein the detection circuit is configured to output a power on signal for the power supply in response to receiving the standby signal;

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a delay circuit coupled to receive the power on signal for the power supply from the detection circuit, wherein the delay circuit is configured to output a delayed power on signal for the power supply in response to receiving the power on signal after a predetermined period of time; and

a stabilizer circuit coupled between the delay circuit and the power supply, wherein the stabilizer circuit is configured to receive the delayed power on signal and to provide the delayed power on signal to the power supply for the delay circuit, wherein the stabilizer circuit is further configured to provide a stable transition from inactive to active for the power up signal at a power supply; and

wherein the delay circuit is configured to provide the delayed power on signal to the power supply as the power up signal once the predetermined period of time has passed since the delay circuit received the power on signal; and

wherein the stabilizer circuit is further configured to receive the standby signal and to provide the standby signal to the power supply as the power up signal to keep the power up signal inactive.
